# **SPECIFICATIONS**

# Dot matrix Display点阵产品规格书





MODEL: CA-SB331615BS

客户承认	
APPROVED	
承认日期	
APPROVED DATE	

# 上海鼎辉科技股份有限公司

SHANGHAI TOPLITE TECHNOLOGY CO.,LTD.

www.ledtoplight.com.cn www.ledtoplite.com

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# TECHNICAL DATA SHEET TOP- CA-SB331616BS <FOR 3.15\*3.4MM /16×15 DOT MATRIX DIGITI>

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### 1. FEATURES

- \* Low power requirement,
- ※ Solid state reliability.
- Wide viewing angle.
- **※** Easy mounting on P.C. boards.
- RoSH compliant.

### 2. DESCRIPTION

- ※ The TOP- CA-SB331615BS is a 2.1 inch (52.5mm) matrix height 16 × 15 matrix display.
- \* This device is made with white dots and black surface.

### 3. APPLICATION

- Digital readout display.
- Instrument panels.
- **※** Elevator.

PART NO.	SIZE	CHIP EMITTED COLOR	FACE COLOR
CA-SB331615BS	3.15*3.4MM /16×15 DOT MATRIX DIGITI	Super Red	Black



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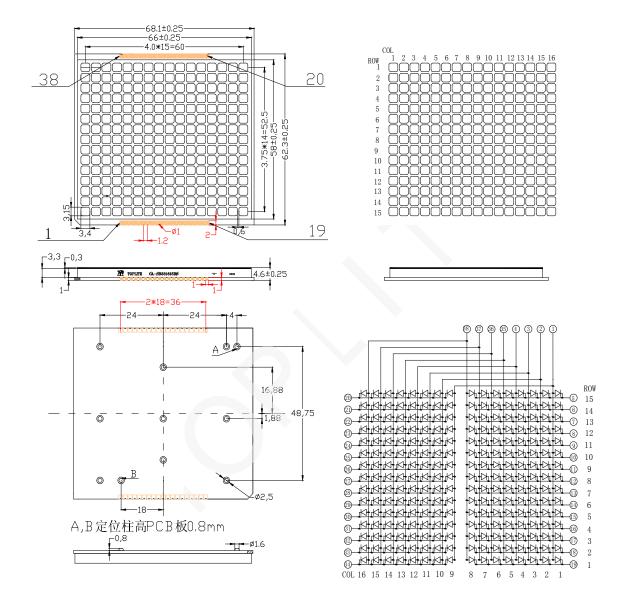
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<FOR 3.15\*3.4MM /16×15 DOT MATRIX DIGITI>

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### 4. PACKAGE DIMENSIONS & CIRCUIT DIAGRAM



Note:

All dimension tolerance is  $\pm 0.25$ mm unless otherwise noted.



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**TOP- CA-SB331616BS** <FOR 3.15\*3.4MM /16×15 DOT MATRIX DIGITI>

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### 5. ELECTRICAL/OPTICAL CHARACTERISTIC

### 5-1. ABSOLUTE MAXIMUM RATINGS (Ta=25℃)

PARAMETER	SYMBOL	VALUE	UNIT
Reverse Voltage	$V_R$	5	V/dot *
Forward Current	$I_{\mathrm{F}}$	20	mA/dot *
Peak Forward Current (1/10 Duty Cycle)	$I_{PEAK}$	120	mA/dot *
Power Dissipation	$P_{D}$	80	mWdot *
Operating Temperature Range	$T_{A}$	- 25 ~ + 85	$^{\circ}$
Storage Temperature Range	${ m T_{STG}}$	- 30 ~ + 85	$^{\circ}$
Solder Temperature	Tsol	260/3	°C/s

### 5-2. ELECTRICAL/OPTICAL CHARACTERISTICS (Ta=25°C)

PARAMETER	SYM	BOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
		R	13500	15525	17549		
Luminous Intensity	IV	S	17550	21938	26325	ucd	I <sub>F</sub> =10mA
		Т	26326	32908	39489		
Forward Voltage	V	F	1.80	2.10	2.40	V/dot *	I <sub>F</sub> =20mA
Dominant wavelength	λ	d	630	-	640	nm	I <sub>F</sub> =20mA
Spectral Line Half-Width	Δ	λ	-	20	-	nm	I <sub>F</sub> =20mA
Reverse Current	I	R	-	-	20-	uA	V <sub>R</sub> =5v

<sup>\*</sup>The dot represents a chip.Each segment according to the principle diagram calculation of voltage and current.



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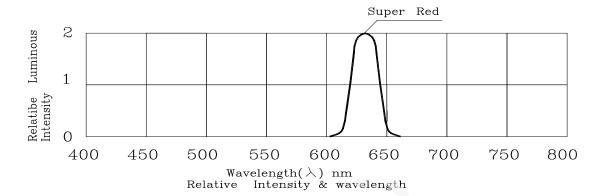
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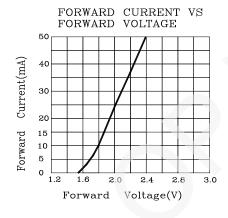
# TECHNICAL DATA SHEET TOP- CA-SB331616BS <FOR 3.15\*3

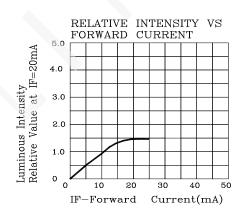
<FOR 3.15\*3.4MM /16×15 DOT MATRIX DIGITI>

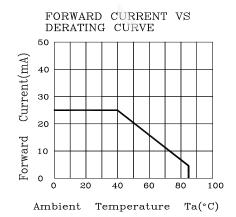
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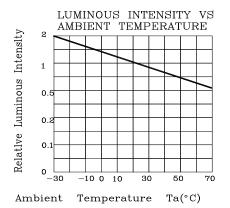
### 5-3. ELECTRICAL/OPTICAL CHARACTERISTIC CURVES













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## 6. QUALITY CONTROL AND ASSURANCE

CLASSIFICATION	TEST ITEM	TEST CONDITION	
	OPERATION LIFE	Ta= Natural temperature  If=12mA-25mA per dot or Ip=80mA/duty=1/8,Pw=1.25mS  Ip=160mA/duty=1/16,Pw=1.mS(DOT)  Test time=1000HRS(-24HRS+72HRS)	
ENDUTRANCE TEST	HIGH TEMPERATURE HIGH HUMIDITY STORAGE	Evaluate storage time of the device under high temperature and high humidity $Ta=65^{\circ}\text{C}\pm5^{\circ}\text{C} \text{ RH}=90\text{-}95\%  \text{Test time}=240\text{HRS}\pm2\text{HRS}$	
	HIGH TEMPERATURE HIGH HUMIDITY REVERSE BIAS	Evaluate leakage current of the device under high temperature and high humidity  Ta=65°C±5°C RH=90-95% VR=5V  Test time=500hrs(-24HRS+48HRS)	
	HIGH TEMPERATURE STORAGE	Evaluate reliability test of the device under high temperature  Ta=85°C±5°C Test time=1000HRS(-24HRS+72HRS)	
	LOW TEMPERATURE STORAGE	Evaluate reliability test of the device under low temperature $Ta=-35$ °C $\pm 5$ °C Test time=1000HRS(-24HRS+72HRS)	
	TEMPERATURE CYCLING	Evaluate thermal expansion and cold contraction of the device under harsh temperature  Ta=85°C~25°C~-35°C~25°C time=30min 5min 30min 5min Cycle test:10cycles	
ENVIRONMENTAL TEST	THERMAL SHOCK	Evaluate structual and mechanical of the device under sudden thermal shock  Ta=85°C±5°C ~-35°C±5°C time=10min 10min Cycle test:10cycles	
	SOLOER RESISTANCE	Evaluate performance of the device withstand thermal shock during soldering  T.sol=260°C±5°C time=10±1sec	
	SOLOER ABILITY	Evaluate solderability of the device T.sol=230°C±5°C time=5±1sec	



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<FOR 3.15\*3.4MM /16×15 DOT MATRIX DIGITI>

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#### 7. SOLDERING CONDITIONS

The recommended conditions for soldering are as follows.

Because the component is made with epoxy resin, the units are susceptible to heat. Therefore,

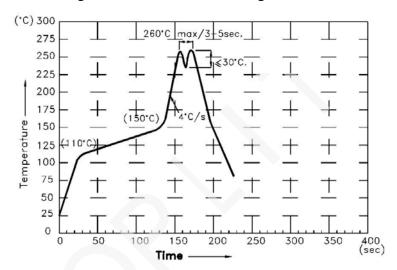
the preheating and soldering temperatures should be kept as low as possible to avoid damage.

**7-1.** Manual Soldering Conditions(with 1.5mm Iron tip ).

Iron Tip Temperature: 350°C Max, Time: 3s Max.

Position: The iron should be situated at least 2mm away from the root of the leads.

**7-2.** Through the Wave Soldering Conditions Wave Soldering Profile For Lead-free Through-hole LED.



#### **7-3.** Soldering General Notes:

- a. Recommend manual soldering to be used only for repair and rework purposes. The soldering iron should not exceed 30W in power. The tip of the soldering iron should not touch the reflector case to avoid heat-damage.
- b. Maintain the pre-heat and peak temperatures with dip units as low as possible and the times as short as is feasible, since the products are susceptible to heat during flow soldering.
- c. After soldering, least three minutes for the component to cool to room temperature before further operations.
- d. If components will undergo multiple soldering processes, or other processes where the components may be subjected to intense heat, please check with toplight for compatibility.



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8. Customer	optional
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\*\*This is a TOPLITE standard specifications(no protective film, TOPLITE printing code).If you have special request, please make the following choices.

8-1. <b>protetive film</b>	add protetive film	do not add protetive film
	protetive film left sidemm	other
*protective film thic	kness standard is 0.1 mm.	
8-2. <b>printing code</b>	TOPLITE standard printing code	
	Special printing code	
	Other	
8-3. Whether need produ	cts with group	
	YES NO	Other
8-4. Other requirements		
a		
b		
c.		